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"register transfer language" lock step
 lockstep processors "register transfer
 "register transfer" lockstep processors
 lockstep block model
 lockstep cores "register transfer
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 synchronized cores "register transfer
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lockstep processors divergence simulation
 lockstep processors divergence verifier
 lockstep processors rtl simulation
 lockstep processors verification model
 modeling "lockstep processor"
 modeling lockstep processors divergence
 multiprocessor model verify synchronization
 processor model verify synchronization
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 register transfer "synchronized processors
 simulating "lockstep cores"
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 simulating lockstep processor
 simulation lockstep verification
 simulation verifying "synchronized processors
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 verification lockstep processor design
 verifying lockstep processors simulation
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 "redundant processors" rtl
 "redundant processors" verification model